

<b>Notice of References Cited</b>	Application/Control No. 09/802,616	Applicant(s)/Patent Under Reexamination HOSKOTE ET AL.	
	Examiner Fred Ferris	Art Unit 2128	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,035,107 A	03-2000	Kuehlmann et al.	716/1
*	B	US-6,687,882 B1	02-2004	McElvain et al.	716/3
*	C	US-6,301,687 B1	10-2001	Jain et al.	716/3
*	D	US-5,754,454	05-1998	Pixley et al.	702/123
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"An Efficient Equivalence Checker for Combinational Circuits", Matsunaga, DAC 96', ACM 1996
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.